## Amendments to claims:

This listing of claims will replace all prior versions and listings of claims in the application:

## **Listing of Claims:**

Claim 1 (Currently amended) A driving circuit for <u>a</u> flat <u>display</u> panel <del>displays</del> <del>disposed on a panel</del>, comprising:

a plurality of video signal lines for providing video signals;

at least one buffer unit for inverting a scanning signal; and

a plurality of switch units disposed between said plurality of video signal lines;

wherein each of said plurality of switch units is connected to at least one <u>video</u> signal line to receive a video signal and is connected to said buffer unit whereby said scanning signal controls the operation of the plurality of switch units and a video signal is outputted to <u>a display area</u> an active area (display area) of said flat panel-display panel.

Claim 2 (Currently amended) The driving circuit of claim 1, wherein said plurality of switch units and said <u>display area active area (display area)</u> of said <u>flat</u> <u>display panel are spaced apart with at least one video signal line.</u>

Claim 3 (Original) The driving circuit of claim 1, wherein said buffer unit for inverting a scanning signal is an inverting circuit receiving a timing signal which is then inverted to output at least one scanning signal.

Claim 4 (Original) The driving circuit of claim 2, wherein said at least one

scanning signal is an inversed signal of said timing signal.

Claim 5 (Currently amended) The driving circuit of claim 1, wherein said

plurality of switch units comprise are thin-film transistors.

Claim 6 (Currently amended) The driving circuit of claim 1, wherein said at

least one video signal line is disposed between said plurality of switch units and

said buffer unit for inverting a scanning signal.

Claim 7 (Currently amended) The driving circuit of claim 1, wherein said

plurality of video signal lines are disposed between said switch units and said

display area active area (display area).

Claim 8 (Currently amended) The driving circuit of claim 1, wherein said flat

display panel comprises is a liquid crystal display panel.